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| | Class | Subclass |
| ISSUE CLASSIFICATION | | |

PATENT NUMBER

U.S. UTILITY Patent Application

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| O.I.P.E. SCANNED <u>Edi</u> Q.A. <u>TM</u> | PATENT DATE |
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| APPLICATION NO. 09/996758 | CONT/PRIOR F | CLASS 438 | SUBCLASS 200 ⁺ | ART UNIT 28124 | EXAMINER H. PHAM |
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ITS Technology Overview

Semiconductor device with two types of FET's having different gate lengths and its manufacture method

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| | | | | | |
| <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____ | _____ (Primary Examiner) (Date) | | | ISSUE FEE | |
| | | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) (Date) | | | ISSUE BATCH NUMBER | |
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Form **PTO-436A**
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